IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ethe Application of: MATSUZAKI, Yasurou et al.

Group Art Unit: 2631

Erial No.: 10/006,238

Examiner: M. H. GHAYOUR

Filed: December 10, 2001

P.T.O. Confirmation No.: 9949

For:

SEMICONDUCTOR WITH CIRCUITRY **FOR EFFICIENT** DEVICE

INFORMATION EXCHANGE

INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 CFR 1.97(b)

RECEIVED

DEC 3 0 2003

Technology Center 2600

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

December 29, 2003

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following the filing date or the entry of the national stage of the abovecaptioned application.

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

U.S. Patent Application Serial No. 10/006,238 Page 2

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP

William L. Brooks
Attorney for Applicant
Reg. No. 34,129

WLB/mla

Atty. Docket No. **011644** Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

23850

PATENT TRADEMARK OFFICE

Enclosures:

PTO-1449

References (2)

European Communication with Partial Search Report

H:\HOME\letitia\WLB\01\011644\ids dec 2003

						T			
INFORMATION DISCLOSURE CITATION PTG 1449			Atty. Docket No. 011644			Serial No	Serial No. 10/006,238		
			Applicant(s): MATSUZAKI, Yasurou et al.						
			Filing Date: December 10, 2001			Group A	Group Art Unit: 2631		
MED 2.0 mm C)									
U.S. PATENT DOCUMENTS									
Examination Docum			ent No. Name	2	Date	Class	Subclass	Filing Date	
								(If appropriate)	
	AA						DI	ECEIVED	
	AB								
	AC							DEC 3 0 2003	
	AD						Techr	nology Center 2600	
FOREIGN PATENT DOCUMENTS									
		Docun	nent No.]	Date	Country		Translation (Yes or No)	
<u> </u>	AE								
	AF								
	AG								
	AH								
OTHER DOCUMENTS									
					11 121 1				
	AI	Comm	enini et al.; "Synthesis of Low-Overhead Interfaces for Power-Efficient ommunication over Wide Buses"; Design automation Conference, 1999 Proceedings, EEE, June 1999, pages 128-133.						
	AJ		a et al.; "High-Level Synthesis with SDRAMs and RAMBUS DRAMs"; <u>IEICE</u> <u>NS. FUNDAMENTALS</u> ; Vol. E82-A, No. 11; November 1999; pages 2347-2355.						
	AK	Report	opy of European Patent Office Communication including Partial European Search eport for corresponding European Patent Application No. 01310305 dated December , 2003.						
Examiner	-			Date Con	sidered				